Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3324	memory and packag$3 and board and (register with address$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:52
S2	1825	memory and packag$3 and (circuit near3 board) and (register with address$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/05/05 15:35
S3	24	(memory and packag$3 and (circuit near3 board) and ((register with address$3) same (control$4 near3 bus))) and (first near3 half) and (second near3 half)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/05/05 15:37
S4	155	((memory near2 module) and pack$4 and (pcb (printed near3 circuit near3 board))) and ((control$4 near3 bus) with register)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:03
S5	458	(memory and module and packag$3) and TSOP	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:06
S6	3	((memory and module and packag$3) and TSOP) and gigabyte	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:09
S7	626	(memory and module and packag$3) and gigabyte	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:10
S8	3683	(memory and module and packag$3) and (wide width) and (height high) and inch$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:11
S9	1	(((memory and module and packag$3) and TSOP) and ((memory and module and packag$3) and (wide width) and (height high) and inch$2)) and (((memory and module and packag$3) and gigabyte) and ((memory and module and packag$3) and (wide width) and (height high) and inch$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:12

S10	51	(memory and module and packag$3) and (bilateral$2 with symmetr$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:13
S11	27	(memory and module and packag$3) and (bilateral with symmetr$6)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:20
S12	1	((memory and module and packag$3) and gigabyte) and (((memory and module and packag$3) and TSOP) and ((memory and module and packag$3) and (wide width) and (height high) and inch$2))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 13:21
S13	76	((memory and module and packag$3) and TSOP) and ((memory and module and packag$3) and (wide width) and (height high) and inch$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 16:41
S14	145	((memory and module and packag$3) and gigabyte) and ((memory and module and packag$3) and (wide width) and (height high) and inch$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 16:55
S15	422	memory and packag$3 and (circuit near3 board) and ((register with address$3) same (control$4 near3 bus))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 17:25
S16	312	(memory and packag$3 and (circuit near3 board) and ((register with address$3) same (control$4 near3 bus))) and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 17:25
S17	25562	memory and module and packag$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 20:03
S18	357	(memory and module and packag$3) and (first adj row) and (second adj row)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 20:04
S19	2	(memory and module and packag$3) and (lateral$2 near2 half)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 20:05

S20	2	(memory and module and packag$3) and (lateral$2 near2 hal$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/07/01 20:05
S21	4101	memory and module and ((IC chip) with arrang$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/18 20:00
S22	894	(memory and module and ((IC chip) with arrang$5)) and ((IC chip) with row)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/18 20:00
S23	0	((memory and module and ((IC chip) with arrang$5)) and ((IC chip) with row)) and (bilateral near2 symmetr$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/18 20:01
S24	17922	"711"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/21 11:49
S25	4521	"711"/$.ccls. and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/21 11:50
S26	1369	("711"/$.ccls. and module) and row	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/21 11:51
S27	775	711/5.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/08/21 12:08
S28	976	memory and TSOP	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/20 18:38
S29	6	(memory and TSOP) and (gigabyte (1-gig) (2-gig))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/20 19:00

| S30 | 23 | US-5012389-$.DID. OR US-5164916-$.DID. OR US-5191404-$.DID. OR US-5383148-$.DID. OR US-5412538-$.DID. OR US-5465229-$.DID. OR US-5495435-$.DID. OR US-5513135-$.DID. OR US-5532954-$.DID. OR US-5642323-$.DID. OR US-5652462-$.DID. OR US-5661339-$.DID. OR US-5691946-$.DID. OR US-5712811-$.DID. OR US-5743408-$.DID. OR US-5847985-$.DID. OR US-5867448-$.DID. OR US-5973951-$.DID. OR US-6072744-$.DID. OR US-6097619-$.DID. OR US-6151235-$.DID. OR US-618100B1-$.DID. OR US-621571B1-$.DID. OR US-622273B1-$.DID. OR US-635353B1-$:DID. OR US-650216B1-$.DID. OR US-654589B1-$.DID. or 2002/088633 or 2003/0014578 or 2003/0051091 or 2003/0057564 or 2003/0061447 or 2003/009879 | US-PGPUB; USPAT | OR | ON | 2004/03/20 18:57 |

S31	21	US-5012389-$.DID. OR US-5164916-$.DID. OR US-5191404-$.DID. OR US-5383148-$.DID. OR US-5412538-$.DID. OR US-5465229-$.DID. OR US-5495435-$.DID. OR US-5513135-$.DID. OR US-5532954-$.DID. OR US-5642323-$.DID. OR US-5652462-$.DID. OR US-5661339-$.DID. OR US-5691946-$.DID. OR US-5712811-$.DID. OR US-5743408-$.DID. OR US-5847985-$.DID. OR US-5867448-$.DID. OR US-5973951-$.DID. OR US-6072744-$.DID. OR US-6097619-$.DID. OR US-6151235-$.DID. OR US-618100B1-$.DID. OR US-621571B1-$.DID. OR US-622273B1-$.DID. OR US-635353B1-$.DID. OR US-650216B1-$.DID. OR US-654589B1-$.DID.	US-PGPUB; USPAT	OR	ON	2004/03/20 18:57
S32	6	(memory and TSOP) and (gigabyte (1-gig) (2-gig) gig)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/20 19:01
S33	149	(711/5.ccls. and module) and (arrang$4 layout)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:14
S34	349	711/5.ccls. and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:14
S35	57	(("711"/$.ccls. and module) and row) and ((ic chip) near3 arrang$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:15
S36	3	(memory and module and ((IC chip) with arrang$5)) and (bilateral near2 symmetr$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:15

S37	128	module and (((IC chip) with arrang$5) with symmetr$7)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:24
S38	84	((memory and module and packag$3) and (first adj row) and (second adj row)) and lateral$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:16
S39	4	(module and (((IC chip) with arrang$5) with symmetr$7)) and (second adj2 (buffer register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 14:24
S40	1	(711/5.ccls. and module) and ((first adj2 (buffer register)) with (first adj2 row)) and ((second adj2 (buffer register)) with (second adj2 row))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 15:00
S41	2579	(memory near2 module) and pack$4 and (pcb (printed near3 circuit near3 board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 15:00
S42	4	((memory near2 module) and pack$4 and (pcb (printed near3 circuit near3 board))) and ((first adj2 (buffer register)) with (first adj2 row)) and ((second adj2 (buffer register)) with (second adj2 row))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 15:01
S43	8	((memory near2 module) and pack$4 and (pcb (printed near3 circuit near3 board))) and ((first adj2 (buffer register)) with row) and ((second adj2 (buffer register)) with row)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 15:02
S44	13	((memory near2 module) and pack$4 and (pcb (printed near3 circuit near3 board))) and ((first adj2 (buffer register)) with (chip Ic (integrated adj circuit))) and ((second adj2 (buffer register)) with (chip Ic (integrated adj circuit)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 15:54

| S45 | 26 | US-4280176-$.DID. OR
US-4679167-$.DID. OR
US-0522636-$.DID. OR
US-5303123-$.DID. OR
US-5428762-$.DID. OR
US-5541448-$.DID. OR
US-5572691-$.DID. OR
US-5631807-$.DID. OR
US-5652861-$.DID. OR
US-5737192-$.DID. OR
US-5835932-$.DID. OR
US-5953738-$.DID. OR
US-6021048-$.DID. OR
US-0657538-$.DID. OR
US-622273B1-$.DID. OR
US-635353B1-$.DID. OR
US-65325B1-$.DID. | US-PGPUB;
USPAT;
EPO; JPO;
DERWENT;
IBM_TDB | OR | ON | 2004/08/14 15:53 |
| S46 | 38 | US-5012389-$.DID. OR
US-5164916-$.DID. OR
US-5191404-$.DID. OR
US-5383148-$.DID. OR
US-5412538-$.DID. OR
US-5465229-$.DID. OR
US-5495435-$.DID. OR
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US-5532954-$.DID. OR
US-5642323-$.DID. OR
US-5652462-$.DID. OR
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US-6151235-$.DID. OR
US-618100B1-$.DID. OR
US-621571B1-$.DID. OR
US-622273B1-$.DID. OR
US-635353B1-$.DID. OR
US-650216B1-$.DID. OR
US-654589B1-$.DID. | US-PGPUB;
USPAT;
EPO; JPO;
DERWENT;
IBM_TDB | OR | ON | 2004/08/14 15:53 |

S47	64	(US-4280176-$.DID. OR US-4679167-$.DID. OR US-0522636-$.DID. OR US-5303123-$.DID. OR US-5428762-$.DID. OR US-5541448-$.DID. OR US-5572691-$.DID. OR US-5631807-$.DID. OR US-5652861-$.DID. OR US-5737192-$.DID. OR US-5835932-$.DID. OR US-5953738-$.DID. OR US-6021048-$.DID. OR US-0657538-$.DID. OR US-622273B1-$.DID. OR US-635353B1-$.DID. OR US-65325B1-$.DID.) or (US-5012389-$.DID. OR US-5164916-$.DID. OR US-5191404-$.DID. OR US-5383148-$.DID. OR US-5412538-$.DID. OR US-5465229-$.DID. OR US-5495435-$.DID. OR US-5513135-$.DID. OR US-5532954-$.DID. OR US-5642323-$.DID. OR US-5652462-$.DID. OR US-5661339-$.DID. OR US-5691946-$.DID. OR US-0571281-$.DID. OR US-5743408-$.DID. OR US-5847986-$.DID. OR US-5867448-$.DID. OR US-5973951-$.DID. OR US-6072744-$.DID. OR US-6097619-$.DID. OR US-6151235-$.DID. OR US-618100B1-$.DID. OR US-621571B1-$.DID. OR US-622273B1-$.DID. OR US-635353B1-$.DID. OR US-650216B1-$.DID. OR US-654589B1-$.DID.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 15:54
S48	2433	(chip IC (integrated adj circuit)) with arrang$3 with opposite	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 16:57
S49	172	((chip IC (integrated adj circuit)) with arrang$3 with opposite) and module and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 16:57

S50	2261	((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) and module and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 16:58
S51	23	(US-6502161-$ or US-6230223-$ or US-6002861-$ or US-5973951-$ or US-5164916-$ or US-6705877-$ or US-5495435-$ or US-6185654-$ or US-6757751-$ or US-6480946-$ or US-4739473-$ or US-6222739-$ or US-6617695-$ or US-6594167-$ or US-6169325-$).did. or (US-20030061447-$ or US-20030051091-$ or US-20030090879-$ or US-20030057564-$ or US-20030014578-$ or US-20020088633-$ or US-20030075789-$ or US-20010050856-$).did.	US-PGPUB; USPAT	OR	OFF	2004/08/14 17:42
S52	0	((US-6502161-$ or US-6230223-$ or US-6002861-$ or US-5973951-$ or US-5164916-$ or US-6705877-$ or US-5495435-$ or US-6185654-$ or US-6757751-$ or US-6480946-$ or US-4739473-$ or US-6222739-$ or US-6617695-$ or US-6594167-$ or US-6169325-$).did. or (US-20030061447-$ or US-20030051091-$ or US-20030090879-$ or US-20030057564-$ or US-20030014578-$ or US-20020088633-$ or US-20030075789-$ or US-20010050856-$).did.) and ((data adj3 bit) near3 range)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:43
S53	0	((US-6502161-$ or US-6230223-$ or US-6002861-$ or US-5973951-$ or US-5164916-$ or US-6705877-$ or US-5495435-$ or US-6185654-$ or US-6757751-$ or US-6480946-$ or US-4739473-$ or US-6222739-$ or US-6617695-$ or US-6594167-$ or US-6169325-$).did. or (US-20030061447-$ or US-20030051091-$ or US-20030090879-$ or US-20030057564-$ or US-20030014578-$ or US-20020088633-$ or US-20030075789-$ or US-20010050856-$).did.) and (bit near3 range)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:43

S54	172	((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) and module and memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:43
S55	0	((US-6502161-$ or US-6230223-$ or US-6002861-$ or US-5973951-$ or US-5164916-$ or US-6705877-$ or US-5495435-$ or US-6185654-$ or US-6757751-$ or US-6480946-$ or US-4739473-$ or US-6222739-$ or US-6617695-$ or US-6594167-$ or US-6169325-$).did. or (US-20030061447-$ or US-20030051091-$ or US-20030090879-$ or US-20030057564-$ or US-20030014578-$ or US-20020088633-$ or US-20030075789-$ or US-20010050856-$).did.) and (bit near3 rang$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:43
S56	1	(((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) and module and memory))) and (bit near3 rang$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:43
S57	7	(((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) not (((chip IC (integrated adj circuit)) with arrang$3 with opposite) and module and memory))) and ("64" adj bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:44
S58	2543	(data adj3 bit) with range	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:55
S59	15	(data adj3 bit) with (fourth adj3 range)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:52

S60	372	((data adj3 bit) with range) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:55
S61	2171	((data adj3 bit) with range) not (((data adj3 bit) with range) and memory and module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:55
S62	372	((data adj3 bit) with range) not (((data adj3 bit) with range) not (((data adj3 bit) with range) and memory and module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:55
S63	252	(((data adj3 bit) with range) not (((data adj3 bit) with range) not (((data adj3 bit) with range) and memory and module))) and (bit near5 range)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 17:56
S64	25959	(buffer register) with (data adj3 bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:26
S65	4990	((buffer register) with (data adj3 bit)) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:26
S66	12236	(buffer register) near3 (data adj3 bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:26
S67	2583	((buffer register) near3 (data adj3 bit)) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:26
S68	518	(second adj3 (buffer register)) near4 (data adj3 bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:53
S69	93	((second adj3 (buffer register)) near4 (data adj3 bit)) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:27

S70	425	((second adj3 (buffer register)) near4 (data adj3 bit)) not (((second adj3 (buffer register)) near4 (data adj3 bit)) and memory and module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:27
S71	93	((second adj3 (buffer register)) near4 (data adj3 bit)) not (((second adj3 (buffer register)) near4 (data adj3 bit)) not (((second adj3 (buffer register)) near4 (data adj3 bit)) and memory and module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:30
S72	30	(((second adj3 (buffer register)) near4 (data adj3 bit)) not (((second adj3 (buffer register)) near4 (data adj3 bit)) not (((second adj3 (buffer register)) near4 (data adj3 bit)) and memory and module))) and ("64" adj bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:30
S73	84	(((buffer register) near3 (data adj3 bit)) and memory and module) and ((second adj3 (buffer register)) near4 (data adj3 bit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:55
S74	440	((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:55
S75	84	(((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:55
S76	356	(((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))) not ((((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))) and memory and module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:56
S77	84	(((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))) not ((((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))) not ((((buffer register) near3 (data adj3 bit)) and ((second adj3 (buffer register)) near4 (data adj3 bit))) and memory and module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 18:56

S78	2146	(address adj2 range) with (register buffer)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:26
S79	421	((address adj2 range) with (register buffer)) and ("64" adj bit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:27
S80	162	(((address adj2 range) with (register buffer)) and ("64" adj bit)) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:26
S81	259	(((address adj2 range) with (register buffer)) and ("64" adj bit)) not ((((address adj2 range) with (register buffer)) and ("64" adj bit)) and memory and module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:26
S82	162	(((address adj2 range) with (register buffer)) and ("64" adj bit)) not ((((address adj2 range) with (register buffer)) and ("64" adj bit)) not ((((address adj2 range) with (register buffer)) and ("64" adj bit)) and memory and module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:26
S83	162	((address adj2 range) with (register buffer)) and ("64" adj bit) and memory and module	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:27
S84	1984	((address adj2 range) with (register buffer)) not (((address adj2 range) with (register buffer)) and ("64" adj bit) and memory and module)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:28
S85	162	((address adj2 range) with (register buffer)) not (((address adj2 range) with (register buffer)) not (((address adj2 range) with (register buffer)) and ("64" adj bit) and memory and module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/14 19:28
S86	4	10/094,512	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/08/16 15:34